

**AMENDMENTS TO THE SPECIFICATION**

Please amend paragraph [0015] as follows:

Figs. [[2 is]] 2A and 2B are [[a]] block diagrams schematically illustrating the configuration of a data output circuit having an off-chip driver circuit according to an embodiment of the present invention;

Please amend paragraph [0023] as follows:

Figs. [[2 is]] 2A and 2B are [[a]] block diagrams schematically illustrating the configuration of a data output circuit including an off-chip driver circuit according to an embodiment of the present invention.

Please amend paragraph [0024] as follows:

A data output circuit 1 shown in Fig. [[2]] 2A includes an off-chip driver circuit 100 having first to N<sup>th</sup> delay circuits 100b; 110-1 to 110-N and first to N<sup>th</sup> off-chip drivers 100a; 100-1 to 100-N, a pre-driver circuit 200, and an output driver circuit 300 connected to output signals of the off-chip driver circuit 100 and the pre-driver circuit 200. In the above, N is a natural number of 2 or more.

Please amend paragraph [0028] as follows:

The delay circuit that can be connected to each of the off-chip drivers may be constructed to delay the data signal (DATA) with the delay circuit added to the input terminal of the off-chip driver, or may be constructed to delay the output signal outputted from the off-chip driver with it added to the output terminal of the off-chip driver as shown in Fig. 2B.